

AI INFRA SUMMIT

September 15-17, 2026 | Santa Clara Convention Center

8,000
ATTENDEES

400
SPEAKERS

250
PARTNERS

8
STAGES



"It was a fantastic event, especially for AI, given its growing interest and relatively new status as a subject area. There were plenty of valuable learnings and best practices shared by industry leaders — overall, a truly great experience."

STAFF ENGINEER, VISA



2025 DIAMOND PARTNERS



IAN BUCK, VP OF HYPERSCALE AND HIGH-PERFORMANCE COMPUTING, NVIDIA



AI DATA CENTER

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A full ecosystem gathering of data center leaders building the next generation of AI capacity, aligning the supply chain to meet rising demand and the rapidly evolving requirements of AI at scale.

Key themes



Operations & Reliability



Power Demands



Cooling Systems



Facility Design



Finance & Investment



Construction & Delivery

Who's Attending?



Technical Program Manager // Solutions Architect // Head of Mission Critical Projects // Network Architect // Site Reliability Engineer // Thermal/Cooling Systems Engineer // Data Center Operations Manager // Director of Capacity Planning // Data Center Design & Construction Manager // Director of Energy & Utilities Engineering // Infrastructure Procurement & Supply Chain Lead // Director of Data Center Development & Site Acquisition

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PHYSICAL AI

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Built for the pioneers in Physical AI focusing on the foundational enabling technology and infrastructure for AI systems that operate in the physical world. A full-stack exploration of cutting-edge research and product design, and multi-disciplinary approaches to robotics & autonomous systems engineering challenges.

Key themes



Physical AI Foundations – Data, Simulation, Infrastructure



Physical AI in Industrial Robotics & Automation



Engineering Challenges in Humanoids



Physical AI Model Development



Sensor Fusion & Multimodal Integration

Who's Attending?



VP, Head of Robotics // Director of Autonomy // Director, Intelligent Automation // Head of Smart Manufacturing // Head of Physical AI // Director, IoT // Autonomous Systems Engineer // Robotics Engineer // Sensor Integration // Deep Learning Optimization Engineer // Computer Vision Engineer // Physics Simulation // Real-time Simulation // Multimodal AI Engineer // Head of Perception // Embedded Hardware Engineer // Embedded Software Engineer // Motion Systems // ML Engineer // Data Scientist



DATA & MODELS

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A cross-industry forum focused on how leading enterprises are moving AI from experimentation into secure, scalable production. Expect to learn real-world deployment lessons across numerous enterprise sectors as AI adoption accelerates.

Key themes



Agentic AI



Privacy and Governance



Optimized AI Infrastructure



MLOps Deployment & Production



Data Quality, Integration and Infrastructure

Who's Attending?



Chief AI Officer // Chief Technology Officer // Chief Data Officer // Software Engineer // Machine Learning Engineer // Data Scientist // Data Engineer // Head/Director/VP of Software Engineering/AI/ML/Data Science/Cloud Engineering/Data & Analytics // Chief AI Architect

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COMPUTE

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Born from our legacy of launching AI hardware startups and bleeding-edge compute products, the compute track is our bread and butter. Join the world's leading AI architects and hardware pioneers making AI training and inference faster, more efficient, and more affordable for enterprises. This is the engine room of AI Infra Summit.

Key themes



Efficient Inference Compute



AI for Chip Design



Scheduling & Orchestration Strategies



Inference Cost Optimization Strategies



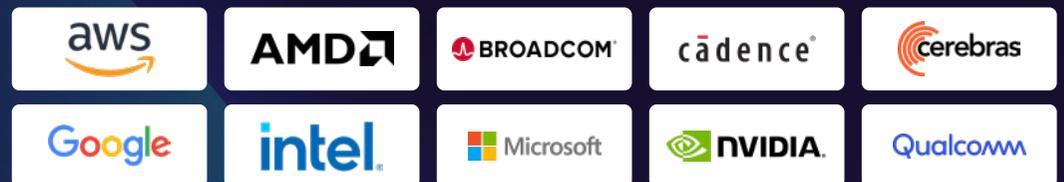
Observability & Monitoring

Who's Attending?



Chief Technology Officer // Chief AI Officer // Chief Data Officer // Chief Information Security Officer // Chief Innovation Officer // SVP/EVP/VP/Head of AI // SVP/EVP/VP/Head of Infrastructure // SVP/EVP/VP/Head of Engineering // SVP/EVP/VP/Head of Hardware // SVP/EVP/VP/Head of Networking // Chief Architect // Director AI Systems // AI/ML Engineer/Architect // Systems Engineer/Architect // Hardware Engineer/Architect // Distinguished Engineer/Architect

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DATA MOVEMENT

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As rapid advances in compute create bottlenecks and walls in systems architectures, data movement is increasingly the primary problem to solve. Join groundbreaking technologists focused on high performance interconnect architectures, novel memory approaches and blisteringly fast storage. This is where data transfer reaches the speed of light.

Key themes



Photonics, Co-Packaged Optics & Optical Computing Advancements



High-Performance Enterprise Storage



Architectural Challenges in HBM



Interconnection Strategies for High Performance AI



Scaling the AI Memory Wall

Who's Attending?



Chief Technology Officer // Chief AI Officer // Chief Data Officer // Chief Information Security Officer // Chief Innovation Officer // SVP/EVP/VP/Head of AI // SVP/EVP/VP/Head of Infrastructure // SVP/EVP/VP/Head of Engineering // SVP/EVP/VP/Head of Hardware // SVP/EVP/VP/Head of Memory // SVP/EVP/VP/Head of Storage // SVP/EVP/VP/Head of Networking // SVP/EVP/VP/Head of Machine Learning // Chief Architect // Director AI Systems // Memory Architect/Engineer // AI/ML Architect/Engineer // Systems Architect/Engineer // Hardware Architect/Engineer // Distinguished Architect/Engineer

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GET IN TOUCH

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